CS 3853 Computer Architecture Quiz 7b Spring 2013

This is a closed book exam. You may not use a computer, but you may use a calculator.
Answer all question on this sheet in the space required.
Each problem is worth 3 points:
   1 point for turning in the paper
   2 points for showing some reasonable work
   3 points for showing work with the correct answer and if it is numeric, putting a box around the correct answer

1) For a paged virtual memory system, what values are typically stored in a page table entry?

2) Assume we have a paged virtual memory system with a 32-bit logical address, a 45-bit physical address, and an 4K page size. How many bits of the logical address are used for the page offset?

3) Assume we have a paged virtual memory system with a 32-bit logical address, a 45-bit physical address, and an 4K page size. What would be stored in a TLB entry? Be sure to indicate the number of bits for each field.